



IN UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 8,112,578

Docket No: 2001-0660.00/US

Issue Date: February 7, 2012

Patentee: Keith R. Slavin

Title: LOW POWER, HASH-CONTENT ADDRESSABLE MEMORY ARCHITECTURE

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
ATTN: Certificate of Correction Branch

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

As the error is that of the Patent Office, it is believed that no fee is due.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

Respectfully submitted,

Customer Number: 26809

Date: February 24, 2012

By: 

Russ Slifer
Reg. No. 39,838

CERTIFICATE UNDER 37 CFR § 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450, on this 24th day of February 2012.

Shellie Bailey

/Shellie Bailey/

Name

Signature

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 8,112,578

Page (1) of 1

DATED : February 7, 2012

INVENTOR(S) : Keith R. Slavin

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 12, lines 56-57, in Claim 1, delete "inputting," and insert - - inputting - -, therefor.

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(Also Form PTO-1050)

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